JUL 0 9 2001

**PATENT** 

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Fabrizio Rovati et al.

Application No.

09/819,940

Confirmation No.

2964

Filed

March 27, 2001

For

COPROCESSOR CIRCUIT ARCHITECTURE, FOR INSTANCE

FOR DIGITAL ENCODING APPLICATIONS

Docket No.

851763.405

Date

July 9, 2001

Box Missing Parts Commissioner for Patents Washington, DC 20231

#### PRELIMINARY AMENDMENT

Commissioner for Patents:

Please amend the above-identified application as follows:

In the Claims:

Please amend claim 49 to read as follows:

49. (Amended) The method according to claim 45, further including:

issuing as voids at least some of the addresses not requiring loading when the absolute coordinates of the predictor block are aligned.

#### **REMARKS**

Claim 49 has been amended to correct a typographical error. It now depends from claim 45 rather than from claim 14. Please enter this amendment prior to examination of the application.

Respectfully submitted,

Fabrizio Rovati et al.

Seed Intellectual Property Law Group PLLC

David V. Carlson

Registration No. 31,153

DVC:jks

701 Fifth Avenue, Suite 6300 Seattle, Washington 98104-7092

Phone: (206) 622-4900 Fax: (206) 682-6031

# VERSION WITH MARKINGS TO SHOW CHANGES MADE

## In the Claims:

Claim 49 has been amended as follows:

49. (Amended) The method according to claim-14\_45, further including: issuing as voids at least some of the addresses not requiring loading when the absolute coordinates of the predictor block are aligned.

194518.1 Doc